

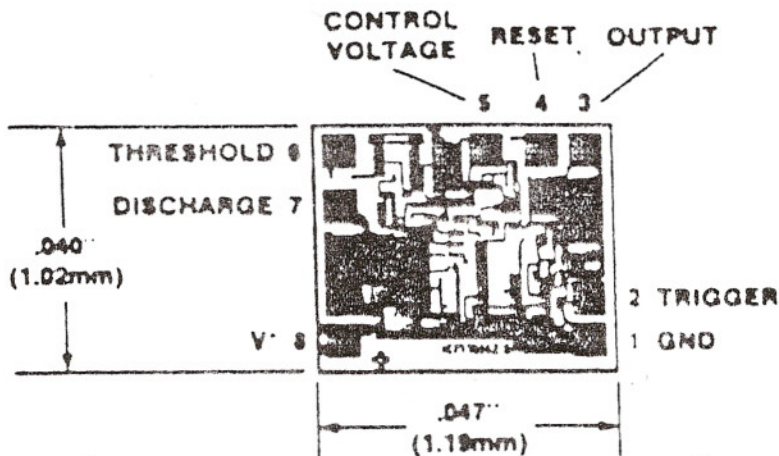


# Sierra Components, Inc.

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Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below.

## ICM7555



**Topside Metal: Aluminum**  
**Backside Metal: Silicon**  
**Backside Potential:**  
**Mask Ref: Issue 1**  
**Bond Pads (Mils): .004" min**

**APPROVED BY:**  
**MFG: Intersil**

**DIE SIZE: .040" X .047"**  
**THICKNESS: .010"**

**DATE: 4/20/00**  
**P/N: ICM7555**